

# PATENT ABSTRACTS OF JAPAN

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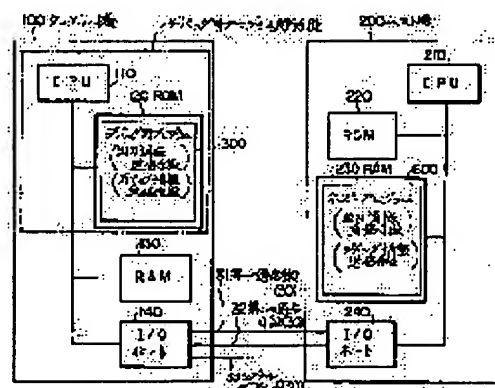
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(54) DEBUG DEVICE AND INFORMATION PROCESSOR AND INFORMATION STORAGE MEDIUM

(57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a debug device in which the debug work of the hardware and software of an information processor including failure in the hardware can be easily attained in a debug stage and after manufacture and shipping, and the debug can be attained regardless of the shape or state of the information processor.

**SOLUTION:** This debug device operates debug by connecting the object of debug(a target equipment 100) through a communication line 30 with a personal computer being a host device 200. In the target equipment 100, a program 300 for debug is executed, the output information is transmitted to the host device 200, and when a command is transmitted from the host device 200, the processing of the command is operated. In the host device 200, a host program 600 is executed, a program is transmitted or executed automatically or according to the instruction of a user, a command for memory reading is transmitted to the target, and the result is received and displayed.



## LEGAL STATUS

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